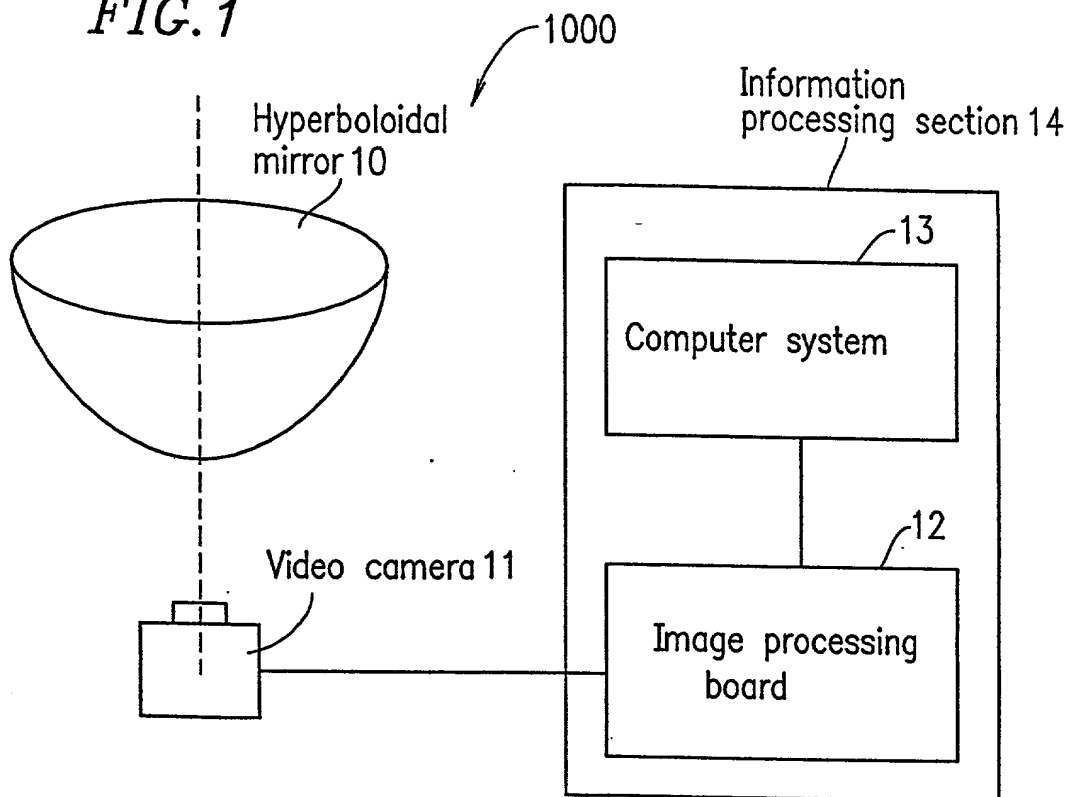
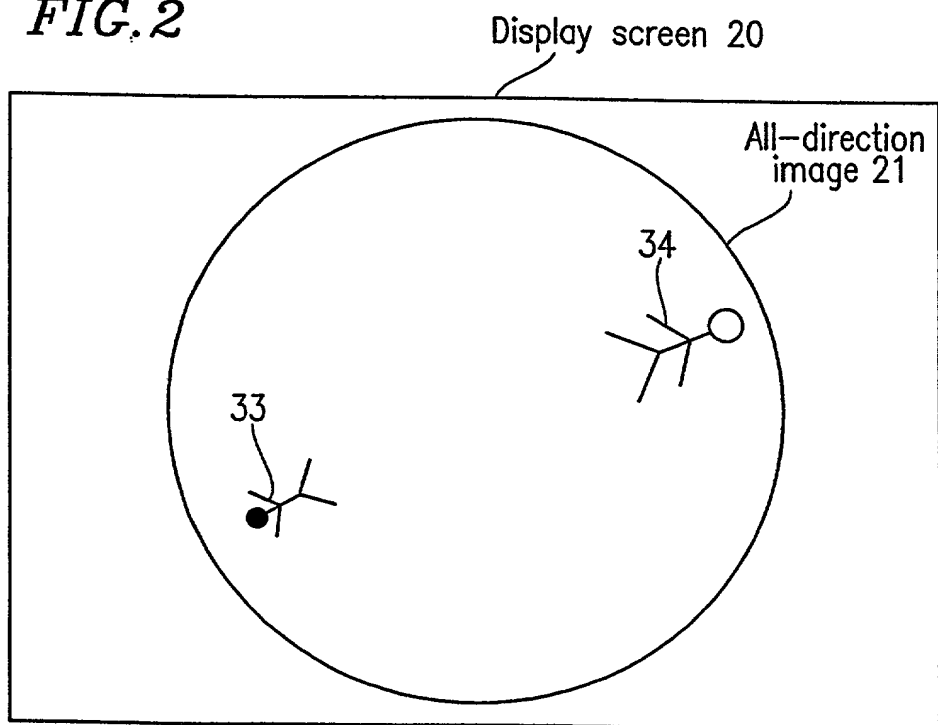


**FIG. 1****FIG. 2**

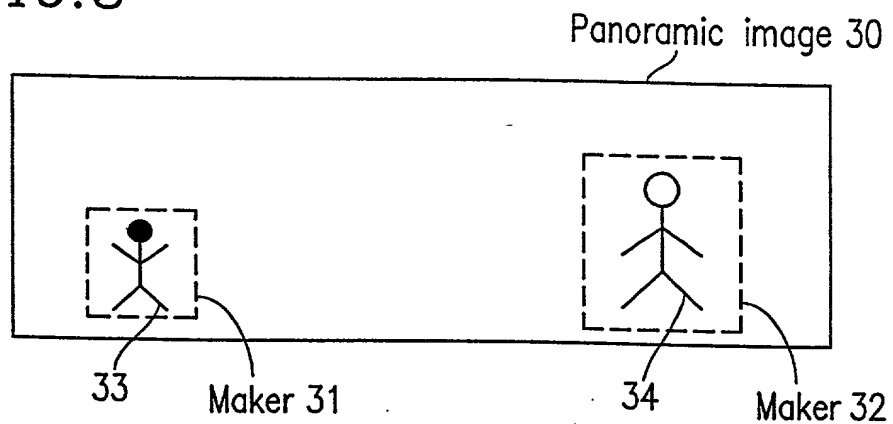
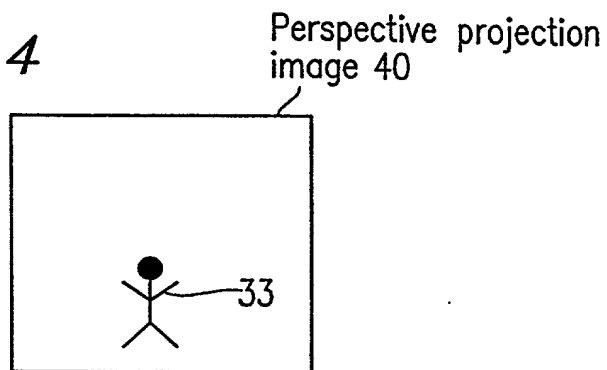
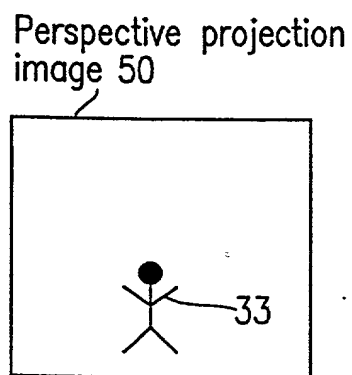
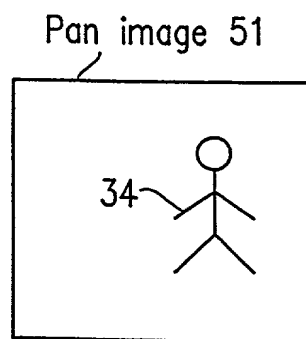
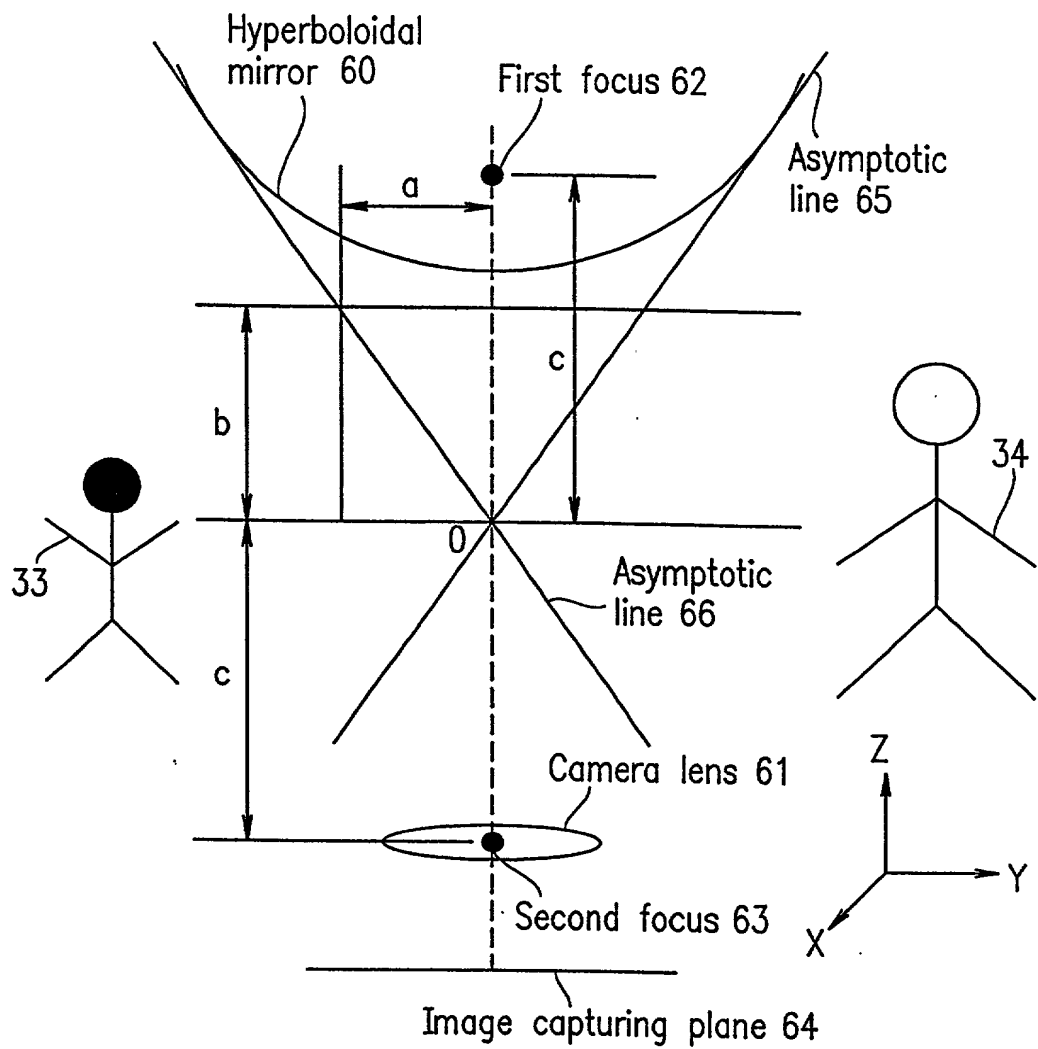
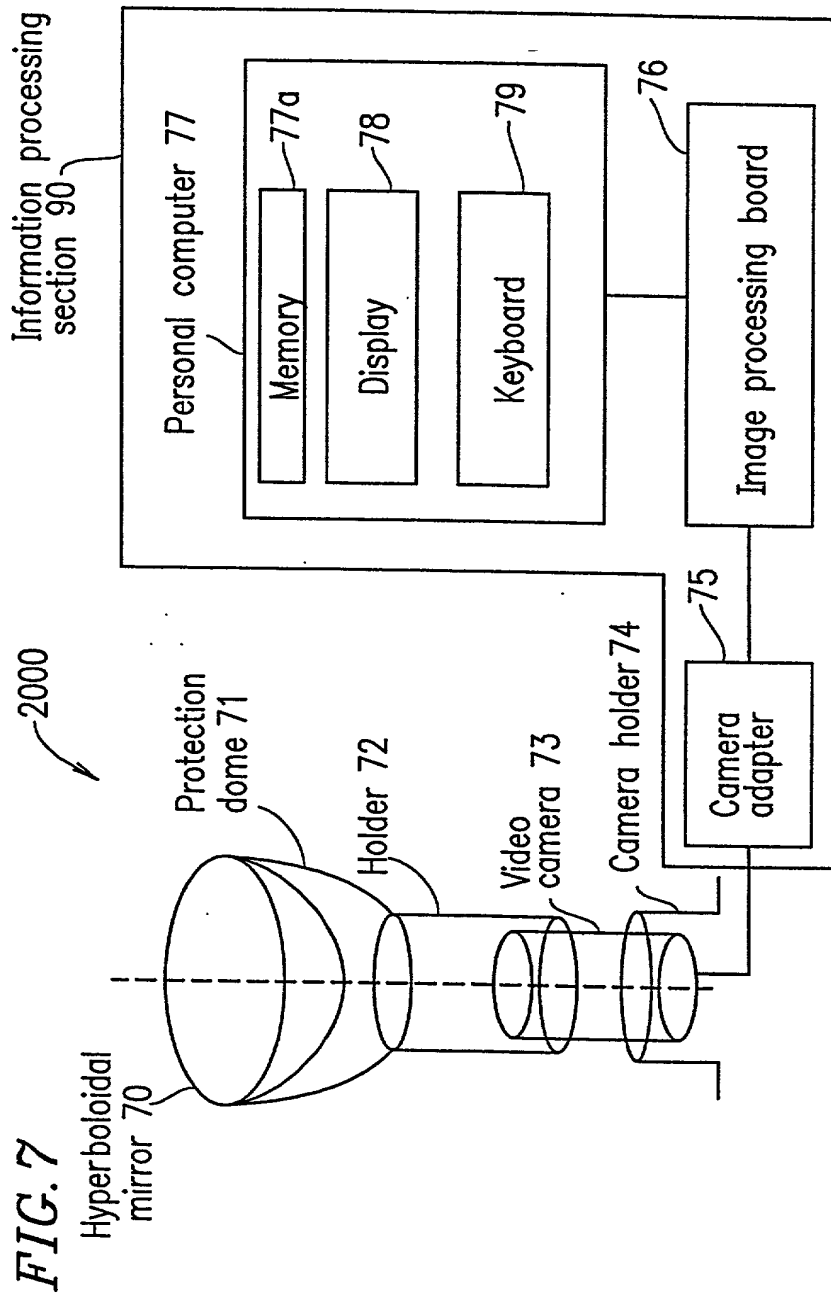
*FIG. 3**FIG. 4**FIG. 5A**FIG. 5B*

FIG. 6





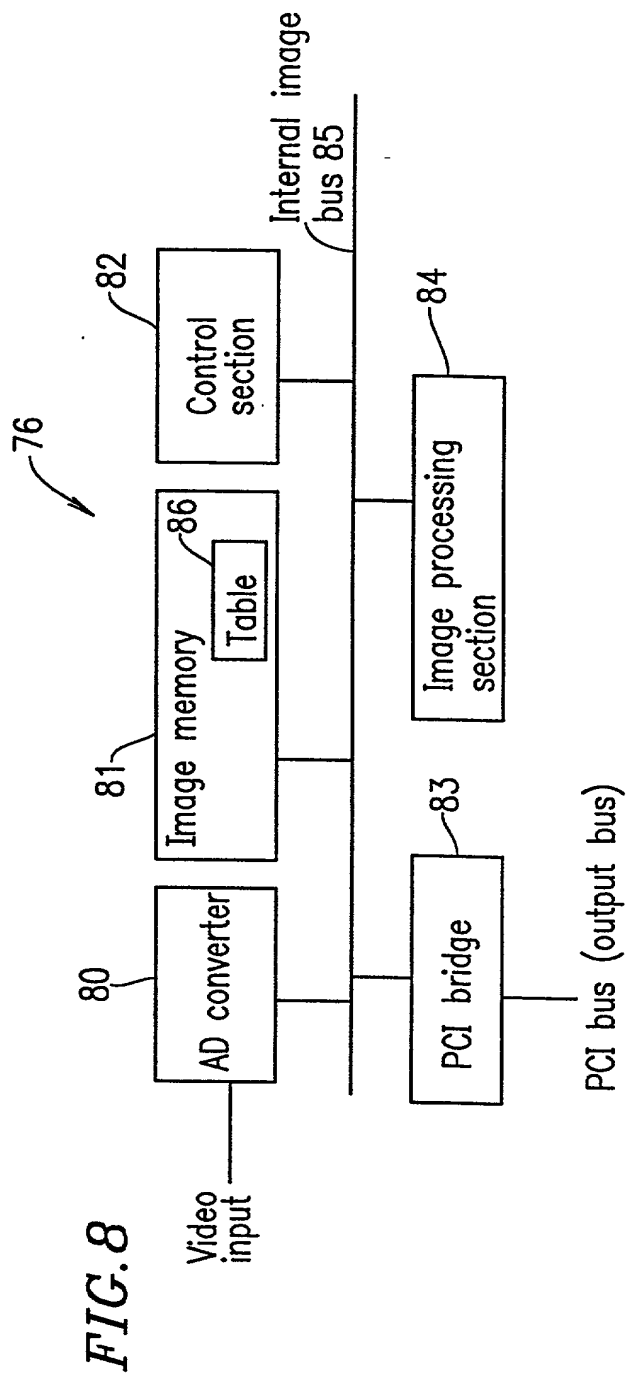


FIG. 8 is a block diagram of an image processing system 76. A video input is connected to an AD converter 80. The AD converter 80 is connected to an internal image bus 85. The internal image bus 85 is connected to an image memory 81, a control section 82, a PCI bridge 83, and an image processing section 84. The image memory 81 includes a table 86. The PCI bridge 83 is connected to a PCI bus (output bus).

FIG. 9



FIG. 10

Perspective  
projection  
image 100

All-direction  
image 91

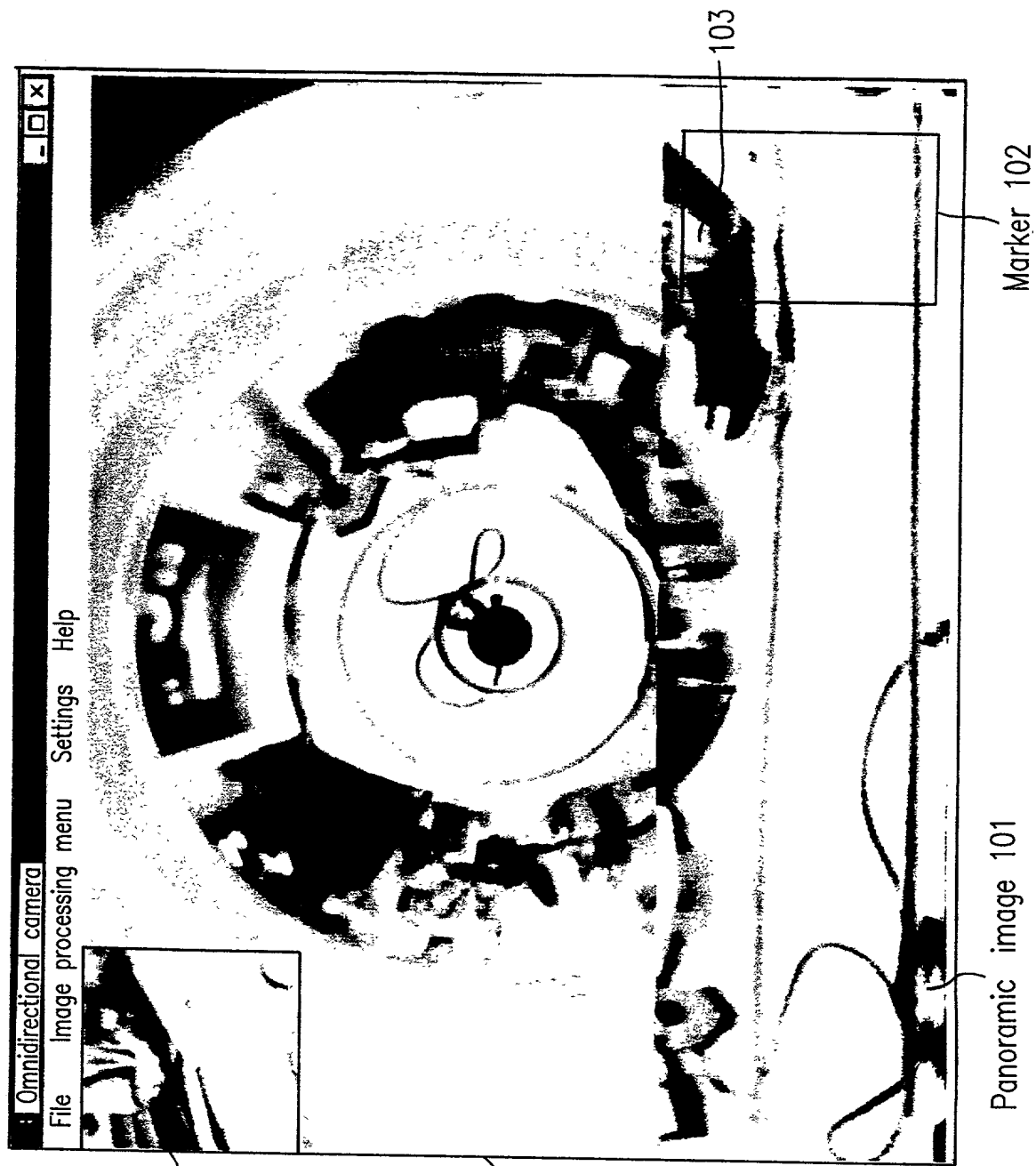
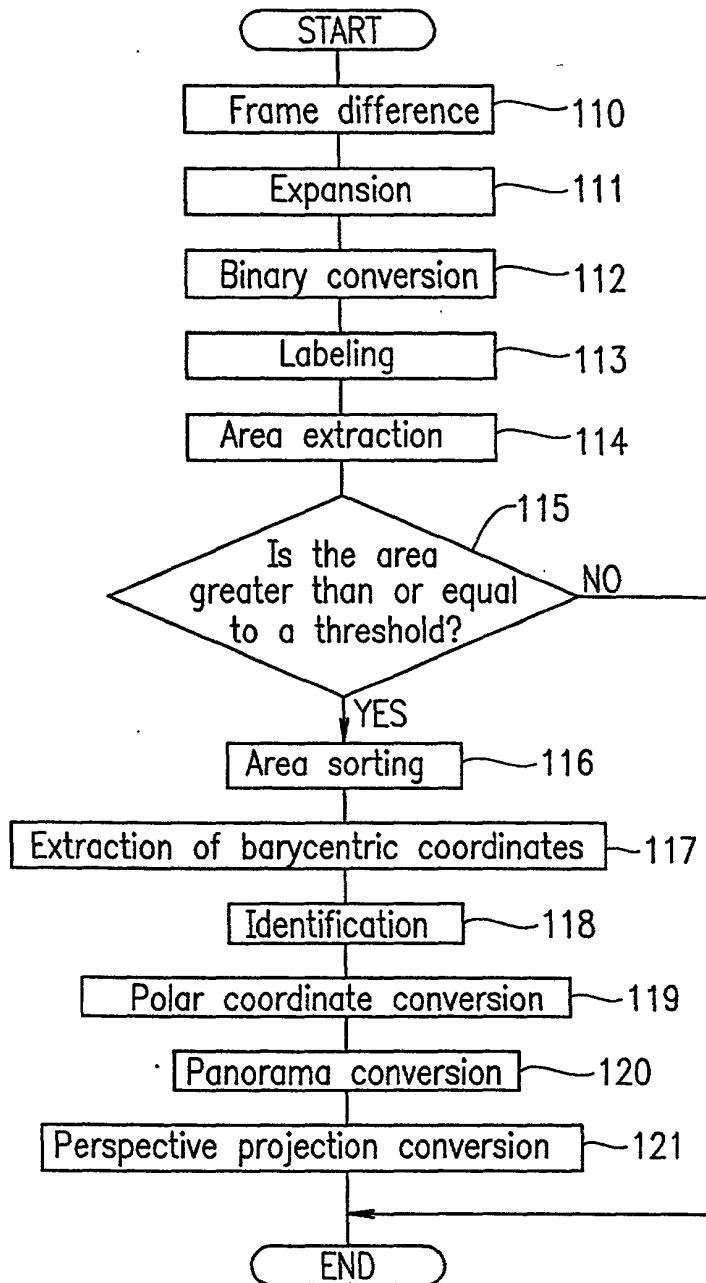


FIG. 11





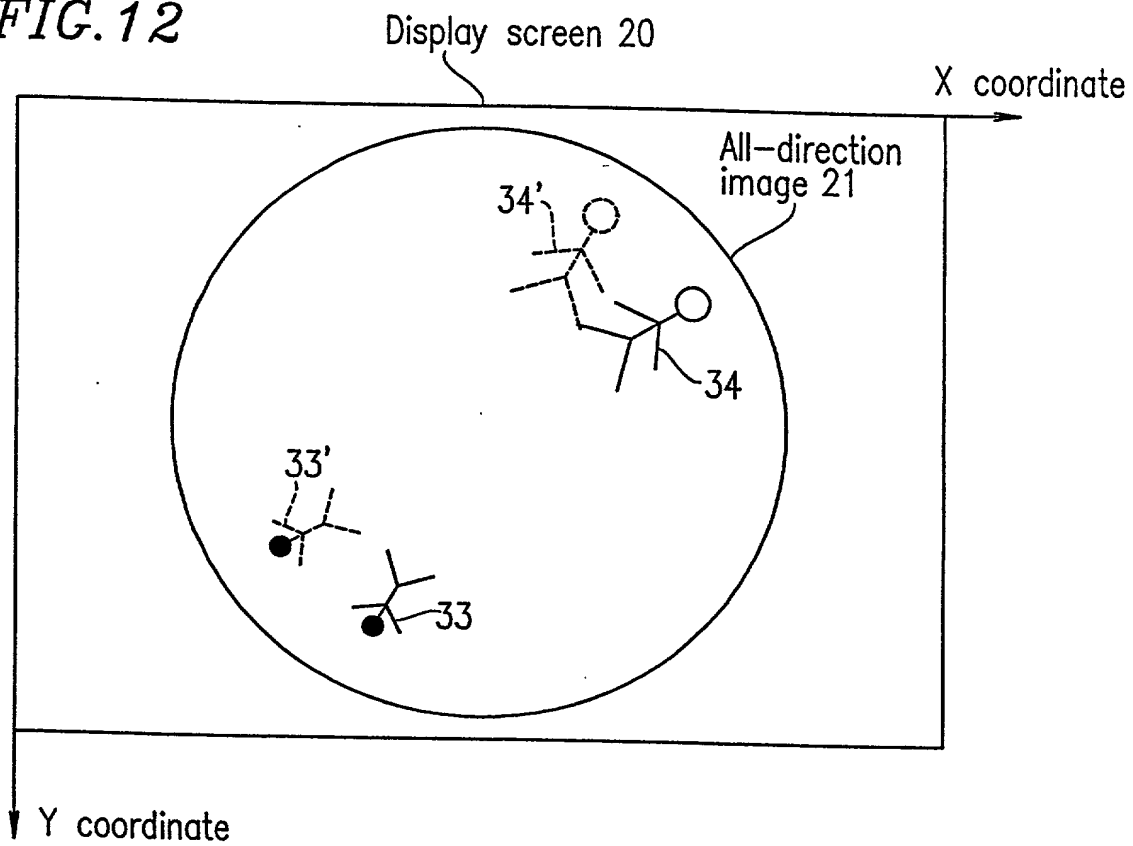
*FIG. 12*

FIG. 13A Display screen 130

All-direction image 131

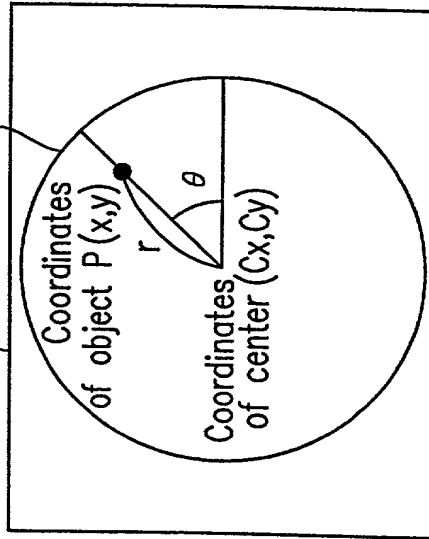
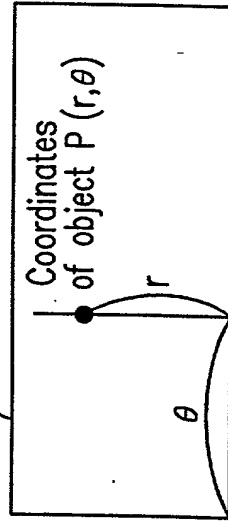
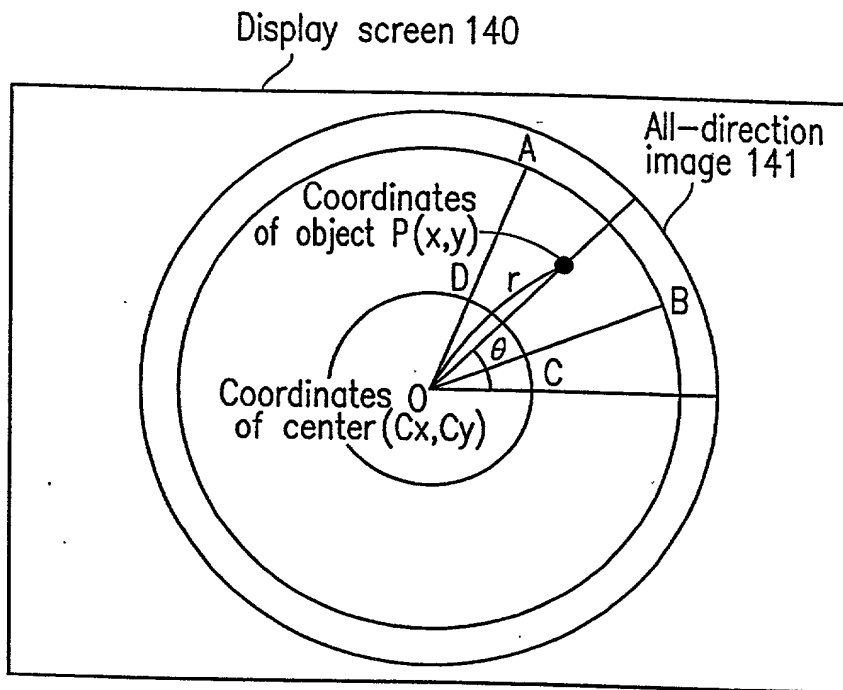


FIG. 13B

Panoramic image 132



**FIG. 14A****FIG. 14B**

Panoramic image 142

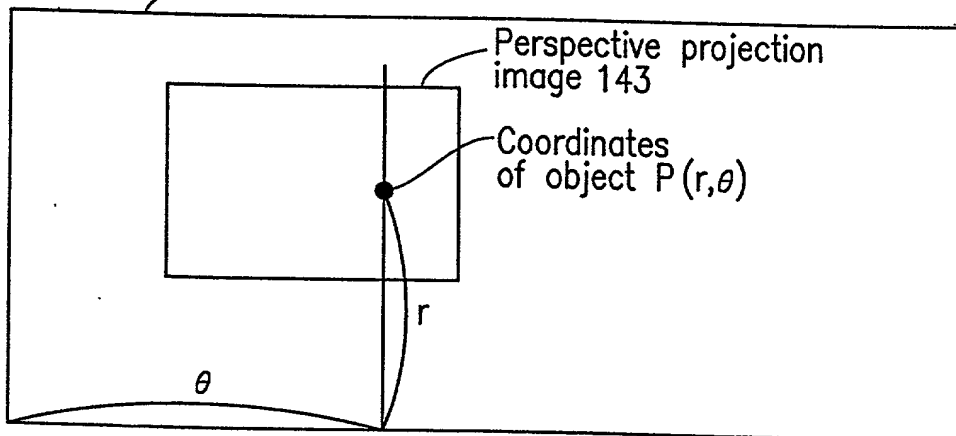


FIG. 15

